

PATENT – AMENDMENT AFTER FINAL  
Response under 37 CFR 1.116  
Expedited Procedure  
Examining Group: 2182

**IN THE CLAIMS:**

1. (Currently Amended) A method for the autonomic configuration of cable speeds in a computing environment, the method comprising:

autonomically reading a cable identifier from an interconnection cable connecting components in the computing environment, wherein the cable identifier contains the length of the interconnection cable;

autonomically storing the cable identifier from the interconnection cable in a software object within the computing environment; and

autonomically adjusting port speeds of components connected by the interconnection cable based on the cable identifier, comprising the steps of:

determining the maximum port speeds of each of the components connected by the interconnection cable;

translating the cable length of an associated interconnection cable to a maximum effective transmission speed for the cable;

if the maximum port speed of any of the components connected to the interconnection cable is less than the maximum effective transmission speed of the cable, adjusting the port speed of the components to the lowest maximum port speed of the components; and

if the maximum port speed of all of the components connected to the interconnection cable is greater than or equal to the maximum effective

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transmission speed of the cable, adjusting the port speed of the components to the maximum effective transmission speed of the cable.

2. (Original) The method of claim 1, wherein the method is triggered upon system bring-up.

3. (Original) The method of claim 1, wherein the method is triggered during run time when the interconnection cable becomes active.

4. (Cancelled)

5. (Currently Amended) The method of claim 1, wherein the cable identifier contains the type of ~~an associated~~ the interconnection cable.

6. (Cancelled)

7. (Currently Amended) The method of ~~claim 4~~ claim 1, wherein one or more pins on ~~an interconnection cable connector~~ a connector of the interconnection cable are jumpered to a first voltage supply and, in conjunction with bias resistors on the connected components, create the cable identifier.

8. (Original) The method of claim 1, wherein at least one of the components is a logically partitioned computer system.

9. (Original) The method of claim 1, wherein at least one of the components is an I/O enclosure.

10. (Cancelled)

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11. (Cancelled)

12. (Cancelled)

13. (Cancelled)

14. (Cancelled)

16. (Cancelled)

17. (Cancelled)